

# The Design and Architecture of MAQAOPROFILE: Assembly Instrumentation Module

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# Outline

- Goals of instrumentation
- Performance tuning platform: MAQAO
- Assembly instrumentation features
- Profiling Scenarii
  - Block level profiling: Hot Paths
  - Loop Level Profiling
  - Instruction Level Profiling
- Performance Advisor
- Conclusion



# Goals of Instrumentation

Where ?

- Instrumentation assembly code
- Instrumentation can focus at several levels of granularity
- On restructured code
- Instrumentation does not disturb the original code

What ?

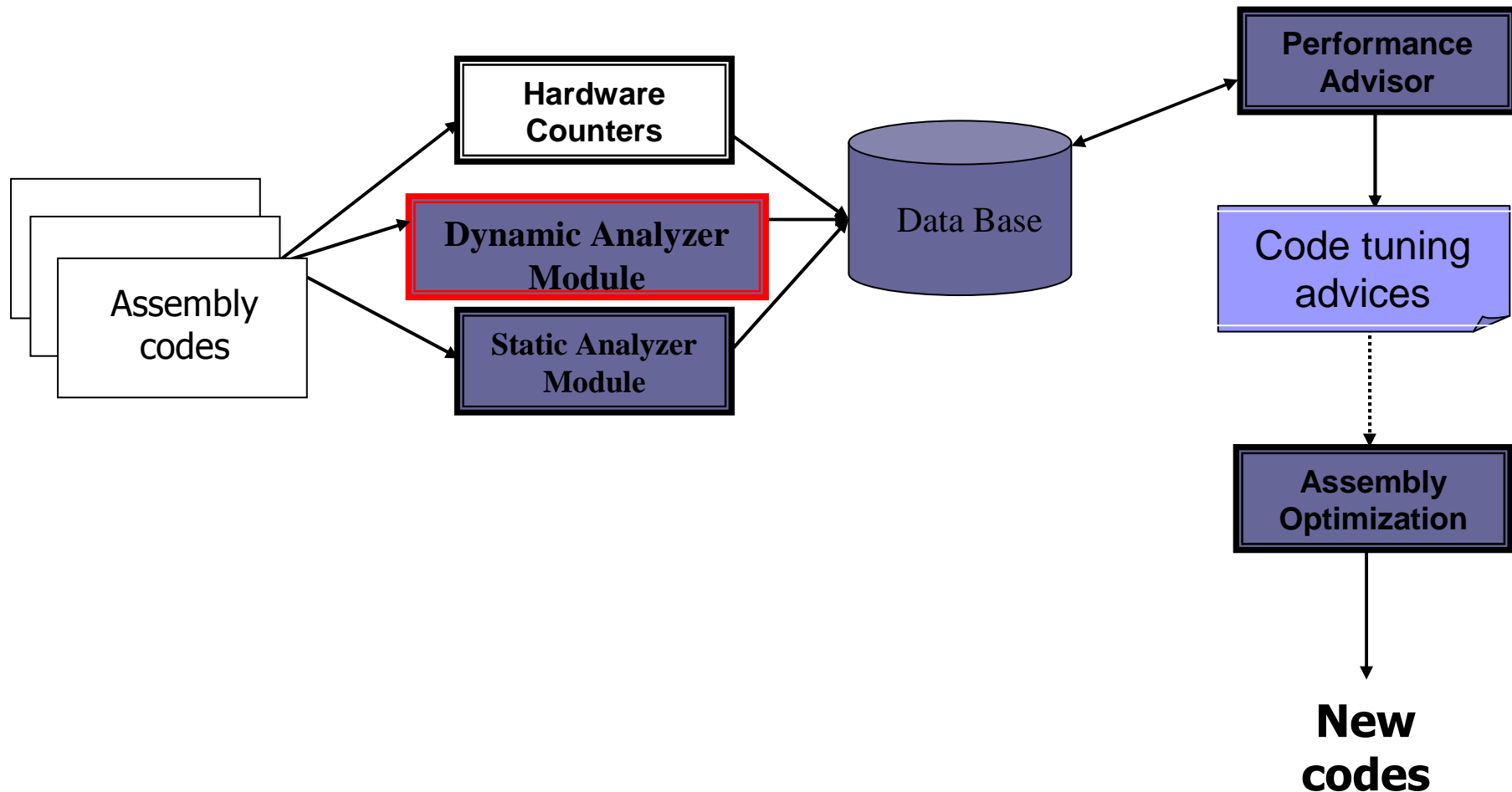
- Time and value profiling

How ?

- Interpretation with performance advisor



# Performance Tuning platform(1)



# Performance Tuning platform(2)

Improving code quality

- Find the parts of code to be optimized
- Detection of inefficient code patterns
- Check the behaviour during execution
- Report results based on static and dynamic analyses



# MAQAO Functionalities

- Navigate through assembly and source codes
- Handle large application codes
- Enable user to drive analysis by scripts
- Designed for interactive and batch mode



# Tuning approach

- Profile (Hardware counters)
- Check for known bad code patterns on hot functions
- Compute bounds from the assembly
  - Memory bound
  - Computation bound
- Compare bounds and compiler schedule:
  - mismatching: code quality problem. Check compiler flag (black belt, source modification)
- Compare profiling info. and compiler schedule:
  - mismatching: dynamic phenomenon. Use hardware counters to solve the issue
- Value Profiling:
  - Function specialization
  - Tuned pipeline depth
- Optimization
- Repeat until performance is at the top



# Instrumentation principle

- Insertion of assembly probes
- Where do we inject these probes ?
- Data collected by probes
  - Builds execution time profile
  - Value profiling
  - Numerous optimization opportunities.





# Features of Instrumentation

- Precise Instrumentation
  - Program/Blocks
    - Execution frequency
    - Execution time
  - Functions
    - Execution time
  - Loops
    - Value profiling
    - Execution time
  - Instructions
    - Value profiling
- Existing scenariii
  - Hotpaths
  - Loops
  - Value profiling of address register
- More scenariii will be developped



## Related Works

- Vtune, Shark, ...
- PIN, ATOM
- HPCVIEW



# Block level profiling: Hot Paths

- Path of the most frequently executed blocks
- Hotpath
  - Frequency computed by probes for each block
  - Detection based on Bellman algorithm



MAQAO

File Analysis Instrumentation Window

Project newtlap.s nwtn\_#

newtlap.s newtlap.f90 Sql IPC Oracle Report Lua Query

Zoom Init Zoom In Zoom Out

```

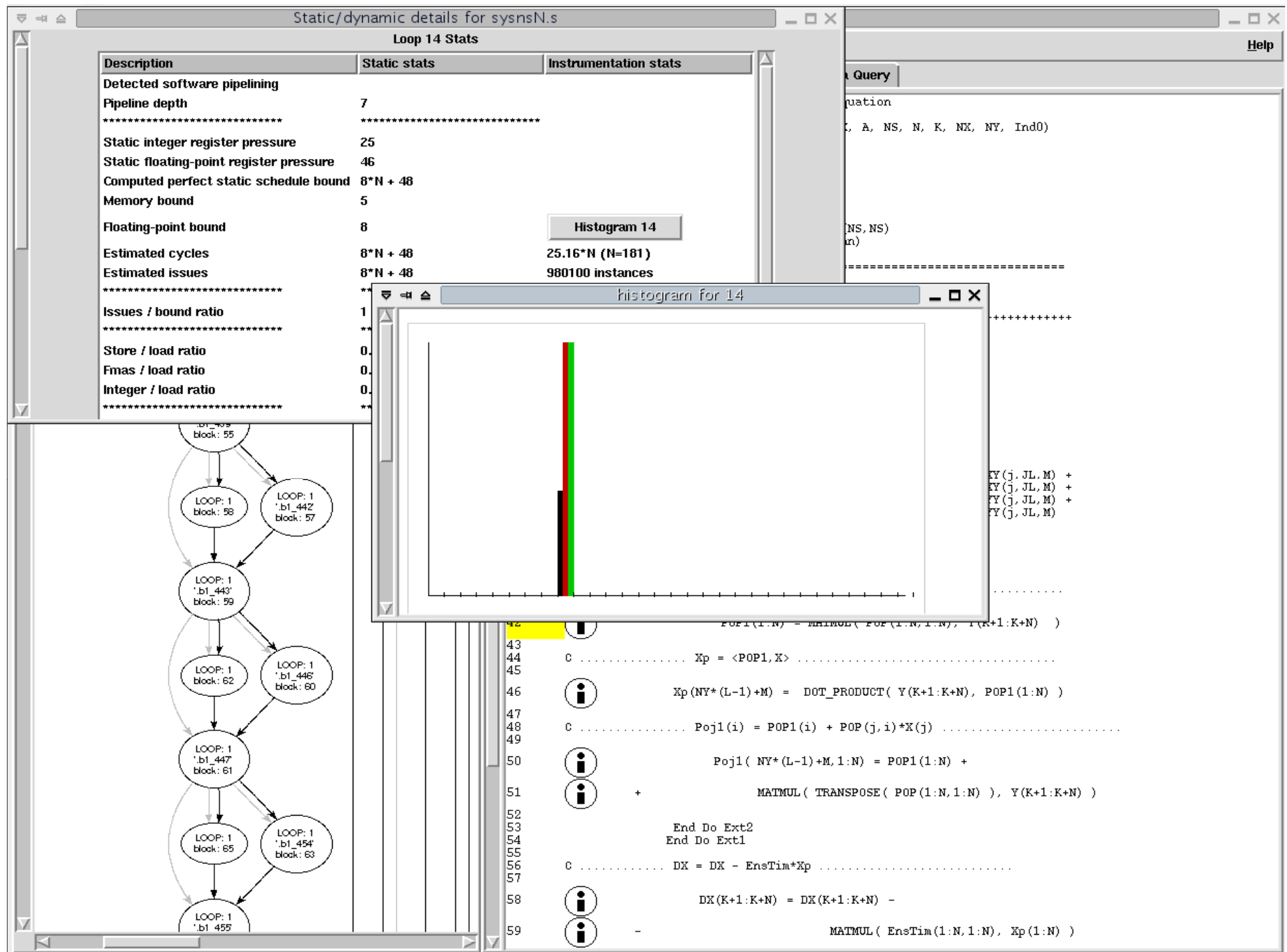
2830      mov     r46=r24 ;;
2831      mov     ar.ec=3 ;;
2832      // Block 104: lentry lexit ltail collapsed pipelined Pred: 104 103 Succ: 104 383 -S
2833      // Freq 5.7e-02
2834      }
2835      .bl_104:
2836      {
2837          (p16) add r56=8, r35 //0: {104:13} 1191
2838          (p16) add r53=16, r35 //0: {104:13} 1198
2839          (p16) add r32=40, r35 //0: {104:13} 1213
2840      }
2841      {
2842          .mmi
2843          (p16) add r50=24, r35 //0: {104:13} 1202
2844          (p16) add r42=32, r35 //0: {104:13} 1209
2845          (p16) add r47=48, r35 ;; //0: {104:13} 1220
2846      }
2847      (p18) stfd [r37]=f74 //17: {104:13} 1190
2848      (p16) ldffd f46=[r53] //1: {104:13} 1199
2849      (p16) add r59=56, r35 //1: {104:13} 1224
2850      {
2851          .mmf
2852          (p16) ldffd f53=[r35] //1: {104:13} 1188
2853          (p16) ldffd f49, f44=[r46] //1: {104:13} 1185
2854          (p18) fms.d f55=f42, f1, f34 ;; //17: {104:13} 1226
2855      }
2856      {
2857          .mmi
2858          (p16) ldffd f56=[r56] //2: {104:13} 1192
2859          (p16) ldffd f59, f34=[r39] //2: {104:13} 1195
2860          nop.i 0
2861      }
2862      (p16) ldffd f42=[r50] //2: {104:13} 1203
2863      (p18) stfd [r58]=f51 //18: {104:13} 1194
2864      nop.i 0 ;;
2865      {
2866          .mmi
2867          (p16) ldffd f64, f51=[r45] //3: {104:13} 1206
2868          (p16) ldffd f66=[r42] //3: {104:13} 1210
2869          nop.i 0
2870      }
2871      {
2872          .mmi
2873          (p16) ldffd f61=[r47] //3: {104:13} 1221
2874          (p18) stfd [r55]=f40 //19: {104:13} 1201
2875          nop.i 0 ;;
2876      }
2877      {
2878          .mmf
2879          (p16) ldffd f68=[r32] //4: {104:13} 1214
2880          (p16) ldffd f71, f32=[r38] //4: {104:13} 1217
2881          (p17) fms.d f73=f54, f1, f50 //12: {104:13} 1189
2882      }
2883      {
2884          .mmi
2885          (p16) ldffd f40=[r59] //4: {104:13} 1225
2886          (p18) stfd [r52]=f39 //20: {104:13} 1205
2887          nop.i 0 ;;
2888      }
2889      {
2890          .mmf
2891          (p16) lfetchn.tl [r41] //5: {104:13} 1228
2892          (p18) stfd [r44]=f38 //21: {104:13} 1212
2893          (p17) fms.d f50=f57, f1, f45 //13: {104:13} 1193
2894      }
2895      {
2896          .mmf
2897          (p18) stfd [r49]=f37 //21: {104:13} 1223
2898          (p16) add r37=64, r38 //5: {104:13} 1236
2899          (p17) fms.d f39=f47, f1, f60 ;; //13: {104:13} 1200
2900      }

```

# Loop Level Profiling

- For inner loops
- Profiling for all loop versions
- Data collected:
  - ☐ whether the loop is executed
  - ☐ Average execution time/iteration
  - ☐ Distribution of loop trip count
  - ☐ Distribution of cycles/iteration
- Provides hot/short loops
- Opportunities for specific optimization
  - ☐ Softwarepipeline tuning depth
  - ☐ Prefetch distance





# Profiling of register values

Capture values of selected registers

- Functions parameters:

- ☐ Detect opportunities for specialization

- Address registers

- ☐ Used to compute prefetch distances
- ☐ Used to detect bank conflicts

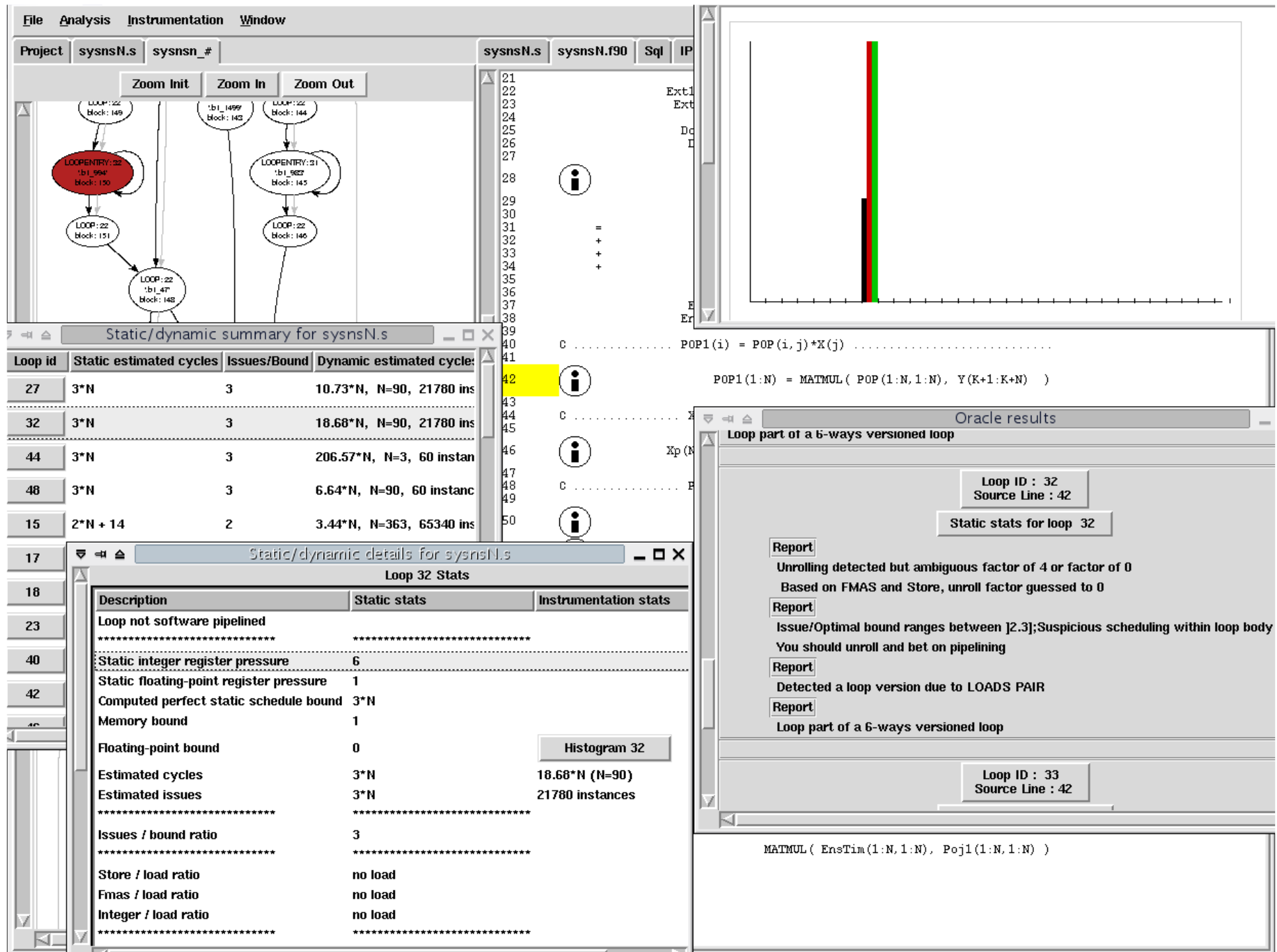


# Performance Advisor

- How to give, manage and extract important tuning information
  - Static Analysis
  - Hierarchical Reporting Approach
- Built over a set of rules and metrics
- Help user to deal with complex architecture
- Provides an optimization report







MAQAO

File Analysis Instrumentation Window Help

Project sysnsN.s sysnsn\_# sysnsN.s sysnsN.f90 Sql IPC Oracle Report Lua Query

Zoom Init Zoom In Zoom Out

Static/dynamic details for sysnsN.s

Loop 2 Stats

Description	Static stats	Instrumentation stats
Floating-point bound	16	
Estimated cycles	$16 \cdot N + 32$	Not executed
Estimated issues	$16 \cdot N + 32$	
Issues / bound ratio	1	
Store / load ratio	1	
Fmas / load ratio	4	
Integer / load ratio	0.37	
Assembly line start	4109	
Assembly line end	4267	
Source line start	28	
	Oracle for line 28	

Oracle results

Static stats for loop 3

Report

Unroll factor detected as 16 high degree of confidence

Based on FMAS and Store number unroll factor guessed to 16

Report

Scheduling is matching optimal bound

This is a clear sign of code quality but not a proof (beware of code bloating)

Report

Detected a loop version due to LOADS PAIR

Report

Vectorization opportunity

Numerous load/store may lead to bank conflict /store queue conflict/secondary miss

You should re-order array indices at the source code level

Report

Loop part of a 4-ways versioned loop

Loop ID : 5

Source Line : 28

```
4114 (p16)ldfd f39=[r79],8 //0: {28:20} 1114
4115 (p16)ldfd f34=[r2],8 //0: {28:20} 1138
4116 (p17)fma.d f55=f7, f40, f49 ;; //16: {30:19} 1134
4117 {
4118 {
4119 (p
4120 (p
4121 (p
4122 {
4123 {
4124 (p
4125 (p
4126 (p
4127 {
4128 {
4129 (p
4130 (p
4131 (p
4132 {
4133 {
4134 (p
4135 (p
4136 (p
4137 {
4138 {
4139 (p
4140 (p
4141 {
4142 {
4143 {
4144 (p
4145 (p
4146 {
4147 {
4148 {
4149 (p18)stfd
4150 (p17)fma.d
4151 nop.
4152 {
4153 {
4154 .mfi
4155 (p17)fma.d
4156 nop.
4157 {
4158 {
4159 .mfi
4159 nop.
4160 (p17)fma.d
4161 nop.
4162 {
4163 {
4164 .mfi
4165 (p17)fma.d
4166 nop.
4167 {
4168 {
4169 .mfi
4170 (p17)fma.d
4171 nop.
4172 {
4173 {
4174 .mfi
4175 (p17)fma.d
4176 nop.
4177 {
4178 {
4179 .mfi
4180 (p17)fma.d
```

# CONCLUSION

## ■ MAQAOPROFILE

- ☐ Delivers precise diagnosis
- ☐ Profiling scenarii
- ☐ Enables user to choose the granularity of the information
- ☐ Generates code tuning advices based on static and dynamic analyses
- ☐ Guides optimization module

## ■ Future work

- ☐ Instrumentation of binary codes
- ☐ Add other rules and metrics to performance adviser
- ☐ Add other optimizations techniques
- ☐ Port MAQAO to other architectures



Thank you

